

# ST330S SERIES

### PHASE CONTROL THYRISTORS

### **Stud Version**

#### **Features**

- Center amplifying gate
- Hermetic metal case with ceramic insulator
- International standard case TO-209AE (TO-118)
- Threaded studs UNF 3/4 16UNF2A or ISO M24x1.5
- Compression Bonded Encapsulation for heavy duty operations such as severe thermal cycling

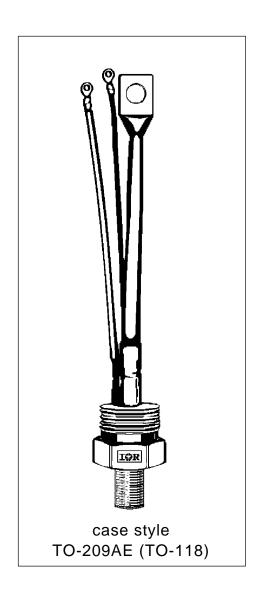
### **Typical Applications**

- DC motor controls
- Controlled DC power supplies
- AC controllers

### Major Ratings and Characteristics

Parameters		ST330S	Units	
I <sub>T(AV)</sub>		330	А	
	@ T <sub>C</sub>	75	°C	
I <sub>T(RMS)</sub>		520	А	
I <sub>TSM</sub>	@ 50Hz	9000	А	
	@ 60Hz	9420	А	
l <sup>2</sup> t	@ 50Hz	405	KA <sup>2</sup> s	
	@ 60Hz	370	KA <sup>2</sup> s	
V <sub>DRM</sub> /V <sub>RRM</sub>		400 to 1600	V	
t <sub>q</sub>	typical	100	μs	
T <sub>J</sub>		- 40 to 125	°C	





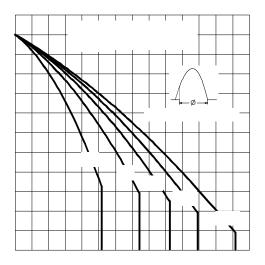
### **ELECTRICAL SPECIFICATIONS**

## Voltage Ratings

Type number	Voltage Code	V <sub>DRM</sub> /V <sub>RRM</sub> , max. repetitive peak and off-state voltage V	V <sub>RSM</sub> , maximum non- repetitive peak voltage V	$I_{DRM}/I_{RRM}$ max. @ $T_J = T_J$ max mA
	04	400	500	
	08	800	900	
ST330S	12	1200	1300	50
	14	1400	1500	
	16	1600	1700	

### **On-state Conduction**

Parameter		ST330S	Units	Conditions			
I <sub>T(AV)</sub> Max. average on-state current		330	Α	180° conduction, half sine wave			
	@ Case temperature	75	°C				
I <sub>T(RMS)</sub>	Max. RMS on-state current	520	Α	DC @ 62°	DC @ 62°C case temperature		
I <sub>TSM</sub>	Max. peak, one-cycle	9000		t = 10ms	No voltage		
	non-repetitive surge current	9420	A	t = 8.3ms	reapplied		
		7570		t = 10ms	100% V <sub>RRM</sub>		
		7920		t = 8.3ms	reapplied	Sinusoidal half wave,	
I <sup>2</sup> t	Maximum I2t for fusing	405		t = 10ms	No voltage	Initial $T_J = T_J$ max.	
		370	KA <sup>2</sup> s	t = 8.3ms	reapplied		
		287	KA S	t = 10ms	100% V <sub>RRM</sub>		
		262		t = 8.3ms	reapplied		
I <sup>2</sup> √t	Maximum I <sup>2</sup> √t for fusing	4050	KA <sup>2</sup> √s	t = 0.1 to 10ms, no voltage reapplied			
V <sub>T(TO)1</sub>	Low level value of threshold voltage	0.91	· V	$(16.7\% \times \pi \times I_{T(AV)} < I < \pi \times I_{T(AV)}), T_J = T_J \text{ max}$ $(I > \pi \times I_{T(AV)}), T_J = T_J \text{ max}.$		$x I_{T(AV)}$ ), $T_J = T_J max$ .	
V <sub>T(TO)2</sub>	High level value of threshold voltage	0.92	V				
r <sub>t1</sub>	Low level value of on-state slope resistance	0.58	mΩ	(16.7% x $\pi$ x $I_{T(AV)} < I < \pi$ x $I_{T(AV)}$ ), $T_J = T_J$ max.		$x I_{T(AV)}$ ), $T_J = T_J$ max.	
r <sub>t2</sub>	High level value of on-state slope resistance	0.57	11122	$(I > \pi \times I_{T(AV)}), T_J = T_J \text{ max.}$		·.	
V <sub>TM</sub>	Max. on-state voltage	1.51	V	$I_{pk}$ = 1040A, $T_J = T_J$ max, $t_p$ = 10ms sine pulse			
I <sub>H</sub>	Maximum holding current	600					
IL	Typical latching current	1000	mA				



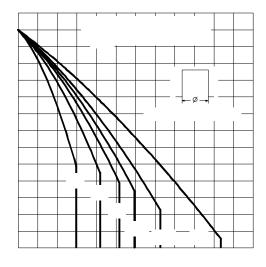


Fig. 1 - Current Ratings Characteristics

Fig. 2 - Current Ratings Characteristics

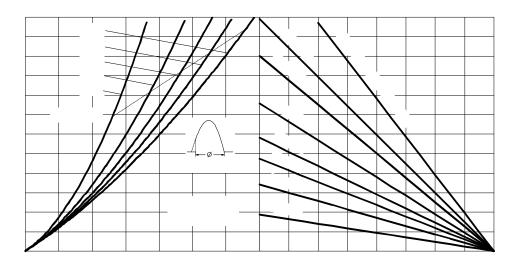


Fig. 3 - On-state Power Loss Characteristics

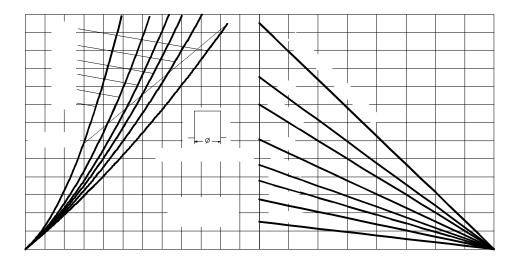
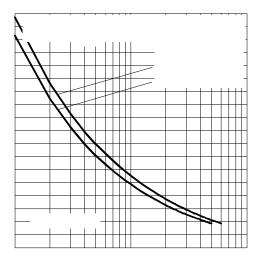


Fig. 4 - On-state Power Loss Characteristics



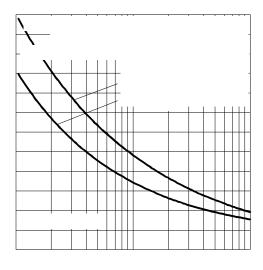


Fig. 5 - Maximum Non-Repetitive Surge Current

Fig. 6 - Maximum Non-Repetitive Surge Current

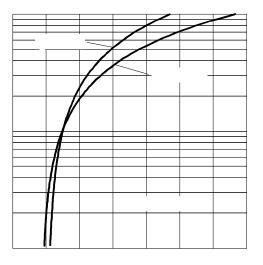


Fig. 7 - On-state Voltage Drop Characteristics

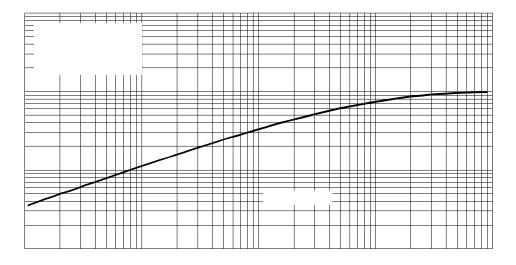


Fig. 8 - Thermal Impedance  $Z_{\text{th.IC}}$  Characteristic

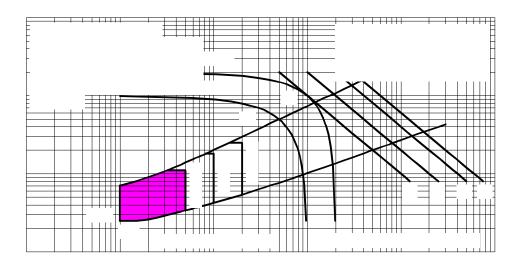


Fig. 9 - Gate Characteristics

## Switching

	Parameter	ST330S	Units	Conditions
di/dt	Max. non-repetitive rate of rise of turned-on current	1000	A/µs	Gate drive 20V, $20\Omega$ , $t_r \le 1\mu s$ $T_J = T_J$ max, anode voltage $\le 80\%$ V <sub>DRM</sub>
t <sub>d</sub>	Typical delay time	1.0		Gate current A, $di_g/dt = 1A/\mu s$ $V_d = 0.67\% V_{DRM}, T_J = 25^{\circ}C$
tq	Typical turn-off time	100	μs	$I_{TM}$ = 550A, $T_J$ = $T_J$ max, di/dt = 40A/μs, $V_R$ = 50V dv/dt = 20V/μs, Gate 0V 100Ω, $t_p$ = 500μs

## Blocking

	Parameter	ST330S	Units	Conditions
dv/dt	Maximum critical rate of rise of off-state voltage	500	V /µs	$T_J = T_J$ max. linear to 80% rated $V_{DRM}$
I <sub>RRM</sub> I <sub>DRM</sub>	Max. peak reverse and off-state leakage current	50	mA	$T_J = T_J \text{ max, rated } V_{DRM}/V_{RRM} \text{ applied}$

## Triggering

	9959							
	Parameter	ST330S		Units	Conditions			
P <sub>GM</sub>	Maximum peak gate power	10.0		w	$T_J = T_J \text{ max, } t_p$	≤ 5ms		
P <sub>G(AV)</sub>	Maximum average gate power	2.	0	l vv	$T_J = T_J \text{ max, } f = 50 \text{Hz, } d\% = 50$			
I <sub>GM</sub>	Max. peak positive gate current	3.	0	Α	$T_J = T_J \text{ max}, t_p \le 5 \text{ms}$			
+V <sub>GM</sub>	Maximum peak positive	0	0					
	gate voltage	2	U	V				
-V <sub>GM</sub>	Maximum peak negative	_	0	V	$T_J = T_J \text{ max}, t_p \le 5 \text{ms}$			
	gate voltage	5.0						
		TYP.	MAX.					
I <sub>GT</sub>	DC gate current required	200	-		T <sub>J</sub> = - 40°C			
	to trigger	100	200	mA	$T_J = 25^{\circ}C$	Max. required gate trigger/ cur-		
		50	-		T <sub>J</sub> = 125°C	rent/ voltage are the lowest value which will trigger all units 12V		
V <sub>GT</sub>	DC gate voltage required	2.5	-		T <sub>J</sub> = - 40°C	anode-to-cathode applied		
	to trigger	1.8	3.0	V	$T_J = 25^{\circ}C$			
		1.1	-		T <sub>J</sub> = 125°C			
I <sub>GD</sub>	DC gate current not to trigger	10		mA		Max. gate current/ voltage not to		
V <sub>GD</sub>	DC gate voltage not to trigger	0.25		V	$T_J = T_J \text{ max}$	trigger is the max. value which will not trigger any unit with rated V <sub>DRM</sub> anode-to-cathode applied		

### Thermal and Mechanical Specification

	Parameter	ST330S	Units	Conditions
T <sub>J</sub>	Max. operating temperature range	-40 to 125	00	
T <sub>stg</sub>	Max. storage temperature range	-40 to 150	°C	
R <sub>thJC</sub>	Max. thermal resistance, junction to case	0.10	K/W	DC operation
R <sub>thCS</sub>	Max. thermal resistance, case to heatsink	0.03	R/VV	Mounting surface, smooth, flat and greased
Т	Mounting torque, ± 10%	48.5 Nm		Non lubricated threads
		(425)	(lbf-in)	Non lubricated threads
wt	Approximate weight	535	g	
	Case style	TO - 209AE (TO-	-118)	See Outline Table

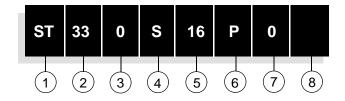
## $\Delta R_{thJC}$ Conduction

(The following table shows the increment of thermal resistence  $R_{thJC}$  when devices operate at different conduction angles than DC)

Conduction angle	Sinusoidal conduction	Rectangular conduction	Units	Conditions
180°	0.011	0.008		$T_J = T_J \text{ max.}$
120°	0.013	0.014		
90°	0.017	0.018	K/W	
60°	0.025	0.026		
30°	0.041	0.041		

### Ordering Information Table

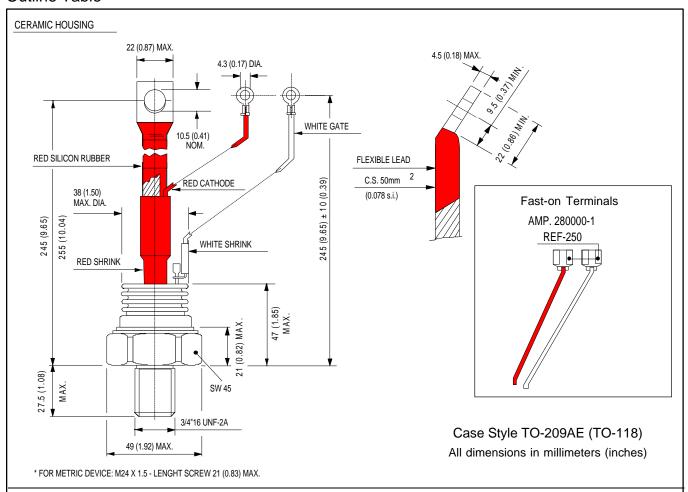
#### **Device Code**



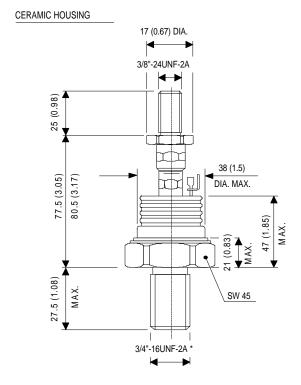
- 1 Thyristor
- Essential part number
- 0 = Converter grade
- S = Compression bonding Stud
- Voltage code: Code x 100 = V<sub>RRM</sub> (See Voltage Rating Table)
- P = Stud base 16UNF threads
  - M = Stud base metric threads (M24 x 1.5)
- 7 0 = Eyelet terminals (Gate and Auxiliary Cathode Leads)
  - 1 = Fast on terminals (Gate and Auxiliary Cathode Leads)
  - 3 = Threaded top terminal 3/8" 24UNF-2A
- Critical dv/dt: None = 500V/µsec (Standard selection)

L = 1000V/µsec (Special selection)

### **Outline Table**



Case Style TO-209AE (TO-118) with top thread terminal 3/8"
All dimensions in millimeters (inches)



 $^{\star}$  FOR METRIC DEVICE: M24 x 1.5 - LENGHT SCREW 21 (0.83) MAX.